Roll No.

A real service with the Party and Pa

322551(22)

B. E. (Fifth Semester) Examination, Nov.-Dec. 2021

(New Scheme)

(CSE, IT Branch)

MICROPROCESSOR & INTERFACES

Time Allowed: Three hours

Maximum Marks: 80

Minimum Pass Marks: 28

Note: Part (a) of each unit is compulsory. Attempt any two parts from (b), (c) and (d).

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	[2]				[3]		
(b)	Draw block diagram of Intel 8085 & explain the			(c)	Explain the concept of segmented memory. Explain		
	following registers:	7			the physical address formation in 8086. What are		
	(i) PC				its advantages?		
	(ii) IR				1900 care to the summer of the substrate and multiple con-		
				(a)	(d) Write a program for the addition of a series of 8-		
	(iii) SP				bit numbers. The series contains 100 numbers.		
	(iv) FLAG				Unit-III		
(c)	What do you understand by Harvard & Princeton architecture of a computer system? Write a short note on microprocessor based personal computer	em? Write a short	3.	(a)	How many INT instructions supported by 8086 microprocessor?		
	system with neat diagram.	7		(b)	What is interrupt vector table of 8086? Explain the		
(d)	What are the basic features of an Intel 8088 micro-				interrupt responce ie ISR sequence of 8086.		
	processor? Write few points on comparision			(c)	Draw and discuss the read & write cycle diagram		
	between 8085 & 8088 microprocessors.	7			of 8086 in minimum mode.		
2. (a)	Unit-II If register AL = FFH & the instruction ADD AL, 1			(d)	What is the use of in asm blocks in assembly language using C/C++? Write a program that uses a character string defined with C and displays		
(1-)	is given, specify the content of the six status flags.	2			showing one word per line.		
(0)	Explain the following Instructions & Assembler directives of Intel 8086. LDA, MOVSB, LOCK				Unit-IV		
	DD, EQU, Stack, PTR.	7	4.	(a)	Pin no. ie. is responsible for		
					operating 8086 microprocessor in minimum & maximum mode.		
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	(b)	Draw & discuss the maximum mode configuration	
		of Intel 8086 microprocessor.	7
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	(c)	Explain the control word formate of 8255 PPI in	
		BSR mode & I/O mode (Mode 0 only).	7
	(d)	Draw & discuss internal architecture of 8253. (Block	
		diagram)	7
		Unit-V	
5.	(a)	Write four features of Intel 80386 microprocessor.	2
		thy What is increasing with examinating at \$1 and problem	
	(b)	Explain the use of each of the following registers of	
		80386.	7
		(i) Segment Descriptor Register	
		(ii) Control Registers	
		(iii) Debug and test Registers and matter and the	
		(iv) System Address Registers	
		a character strong defined with C and displa-	
	(c)	What do you mean by Paging? What are its	W
		advantages and disadvantages?	7
	(d)	Write short notes on the following: (any two)	7
		(i) Pentium Pro.	şĀ
	Z	(ii) Core i3	
0		PERSONAL PROPERTY.	
100	1	(iii) Atom processors 322551(22)	
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